

# DL-DAQ-902 Technical Specification

## Ultra-Dynamic Signal Testing System

Ultra-dynamic signal testing system for shock and blasting tests, supporting stress-strain, bridge-type force, pressure, displacement, velocity, acceleration, charge, and voltage signals, 8M-point per-channel cache, 20 MHz transient sampling, DC to 1 MHz frequency response, gigabit Ethernet expansion, multiple trigger modes, 16-bit A/D conversion, four-wire bridge excitation, and high-speed data transfer.

System Category	DL-DAQ
Signal Type	Ultra-dynamic acquisition
Measurement Range	Stress-strain, force, pressure, displacement, velocity, acceleration, charge, and voltage
Sampling / Response	20 MHz transient sampling; DC to 1 MHz response
Communication	Gigabit Ethernet expansion
Protection / Enclosure	Shock and blasting test environment
Power Supply	System cabinet power
Installation	Ultra-dynamic DAQ chassis

### Key Features

- 20 MHz transient sampling and 1 MHz response support shock and blasting signal capture.
- Per-channel cache supports high-speed transient recording.
- Manual, external, and signal trigger modes support event-based testing.
- Technical basis may reference GB/T 6587 environmental requirements.

### Typical Use Cases

- Shock, blasting, and transient signal testing.
- High-speed structural, vehicle, weapon, and mechanical impact experiments.

### Deployment Notes

- Confirm transient sampling rate, trigger mode, cache depth, signal conditioning, and transfer rate.
- Coordinate cable shielding, grounding, sensor power, and safety procedures for shock tests.
- Remove original customer cases and user lists from any public-facing content.

### Technical Highlights and Standards

- 20 MHz transient sampling
- DC to 1 MHz response
- 8M points per channel cache

- 16-bit A/D conversion
- Gigabit Ethernet
- GB/T 6587 reference

Branding, supplier names, phone numbers, email addresses, physical addresses, logos, customer lists, prices, and original supplier model identifiers have been intentionally excluded from this public specification.